

Prasad V. Potluri Siddhartha Institute of Technology

Kanuru, Vijayawada- 520 007, Andhra Pradesh, India. www.pvpsiddhartha.ac.in

Accredited with A+ grade by NAAC

AUTONOMOUS, ISO 9001:2015 Certified Institute, Permanently Affiliated to JNTUK, KAKINADA

All UG Programmes are provisionally accredited by NBA Tier-1

One Week Online Faculty Development Programme

on

"Design and Development of Control Strategies on Electrical Power Applications - An Industrial Perspective"

07th to 12th December 2020

Organized by

Department of Electrical and Electronics Engineering

Sponsored by

Siddhartha Academy of General & Technical Education, Vijayawada Chief Patrons

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Dr. N. Vijaya Anand, Associate Professor

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Mr. T. Srinivasarao, Assistant Professor

ABOUT THE INSTITUTE

Prasad V Potluri Siddhartha Institute of Technology is established in 1998 with a great vision of producing quality technocrats, the college achieved its dream through meticulous planning of the administrators and the strenuous effort of the faculty. The Institution has attained success in creating the zeal for learning in the students. It is permanently affiliated to Jawaharlal Nehru Technological University, Kakinada. Andhra Pradesh, India. As a result of its concentrated efforts to attain quality, it is conferred Autonomous status in 2012. The college is certified by ISO 9001:2015, accredited by National Assessment and Accreditation Council with A+ grade and all UG programs are accredited by NBA under Tier-I.

ABOUT THE DEPARTMENT

The department of Electrical and Electronics Engineering has been established in the year of 2001. The department is reaccredited by the National Board of Accreditation in 2019 and is certified by ISO 9001:2015 certification. The annual intake of the students is 120+24 in undergraduate course. The department consists of 3 professors, 5 Associate Professors, 19 Assistant Professors. The department has been recognized as research center by JNTUK, Kakinada, India. The department organized various programs such as skill development, hands on sessions and technical workshops to strengthen the overall development of the students and faculty.

ABOUT THE FACULTY DEVELOPMENT PROGRAMME

This programme has been designed to train and develop the faculties/Research scholars with pertinent Industry skills to impart/demonstrate the new-fangled control strategies on Electrical power applications, thereby functioning as a resource in guiding and motivating faculty, P.G students and research scholars alike. The main objective is to fill the gap between academy and Industry, thereby improving independently or team to design an electrical power application module. The following Resource persons are encompassing with this FDP.

EMINENT SPEAKERS



Mr. Anand R C, Head - Systems Technology, **ZIV Automation, Bengaluru**

Protection Relay & Substation Automation System

Dr. B.Chitti Babu, Assistant Professor **IIITDM Kancheepuram, Chennai**

Power Electronics Control Techniques for Grid-Connected Photovoltaic Inverter under Unbalanced Grid Voltage Conditions

Mr. S.Selvakumar, Head - Engineering & Design **Power Projects, Chennai**

TOPIC

Real Time case studies in Power System Integration

Dr. Vishwanathan N, Professor,

NIT Warangal

TOPIC Modelling of DC to DC Converters and **Current Mode Control**

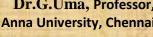


Mr. Rathnakumar Devaraj, Industrial & Systems Engineer **CE+T Power, Belgium**

TOPIC

Modular Inverters & Applications

Dr.G.Uma, Professor, Anna University, Chennai





TOPIC Power Quality Studies in a Hybrid Micrigrid



Mr. A G Vishal Anand, Principal Engineer, Bloom Energy, Bengaluru

TOPIC

Grid Integration of Inverters

Dr. D. R. Binu Ben Jose, Associate Professor

VIT, Chennai

TOPIC A Design Approach to Renewable Energy Systems



Mr.Saravanan Brahmanandam, Lead-Traction Control, Alstom Transport India Ltd, Bengaluru

Power Electronics in Traction Applications

Dr.Sivasankari Sundaram, Assistant Professor **Energy Institute RGIPT-Bangalore**

Control Strategies of Power Train Applied to EVT





Dr. T. Sudhakar Babu, Postdoctoral Researcher, Institute of Power Engineering, Dept, of Electrical Power Engineering, Universiti Tenaga Nasional (UNITEN), Malaysia.

Effect of partial shading and its mitigation techniques for solar PV systems

Dr.M.V.Ramesh, Associate Professor Prasad V. Potluri Siddhartha Institute of Technology, Vijayawada



An Overview on Electrical Vehicle

No Registration Fee

Register Here

https://docs.google.com/forms/d/e/1FAIpQLSdTbnv2Nyf8-SSJ5jQj_acMnVlsvgPlwnSUlkjn5QbgL6pUZA/viewform?usp=sf_link **E-Certificate** will be provided